Electronic Acknowledgement Receipt			
EFS ID:	3120314		
Application Number:	10764003		
International Application Number:			
Confirmation Number:	8941		
Title of Invention:	Method and system for phase offset cancellation in multi-phase clock generator circuits		
First Named Inventor/Applicant Name:	Ramin Farjad-rad		
Correspondence Address:	SAWYER LAW GROUP LLP - P.O. Box 51418 - Palo Alto CA 94303 US 650-493-4540 -		
Filer:	Joseph A. Sawyer		
Filer Authorized By:			
Attorney Docket Number:	3003P		
Receipt Date:	08-APR-2008		
Filing Date:	22-JAN-2004		
Time Stamp:	15:28:46		
Application Type:	Utility under 35 USC 111(a)		
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Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
	Drawings-other than black and white	3003P_ReplacementDrawin gs.pdf	240467	no	6
	line drawings		01aebd82278b28a45401900b33c4cf73 ea9ab8af		

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2	Amendment After Final	3003P_AmendmentAF.pdf	46610 8cd978b9eb909a06daab4a23c74a010 616f2d288	no	13
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